

MICRO-DISPLAY 2x16st. STN LCD 3mm, LED-BELEUCHTET

second source !



Abmessungen: 53 x 20 mm

TECHNISCHE DATEN

- * KONTROLLER IN CHIP-ON-BOARD TECHNOLOGIE, HD44780-KOMPATIBEL
- * 4- ODER 8 BIT DATENBUS + STEUERLEITUNGEN E, RS, RW
- * VERSORGUNG 5V / typ. 1,8mA
- * KONTRASTREICHES SUPERTWIST-DISPLAY (YELLOW/GREEN-MODE)
- * LED-HINTERGRUNDBELEUCHTUNG (LIGHT-PIPE), R_{LED} EXT., 35-40mA@4,1V
- * BETRIEBSTEMPERATUR 0°C ... 50°C
- * LAGERTEMPERATUR -20°C ... +70°C
- * NULLKRAFTSTECKER ON-BOARD
- * SECOND SOURCE ZU BATRON BT21603 BZW. POWER TIP PC1602-K
- * VERBESSERTE BELEUCHTUNG
- * HOCHWERTIGER ONBOARD-NULLKRAFTSTECKER
- * STANDARDDISPLAY: DESHALB IN DER REGEL AB LAGER LIEFERBAR !

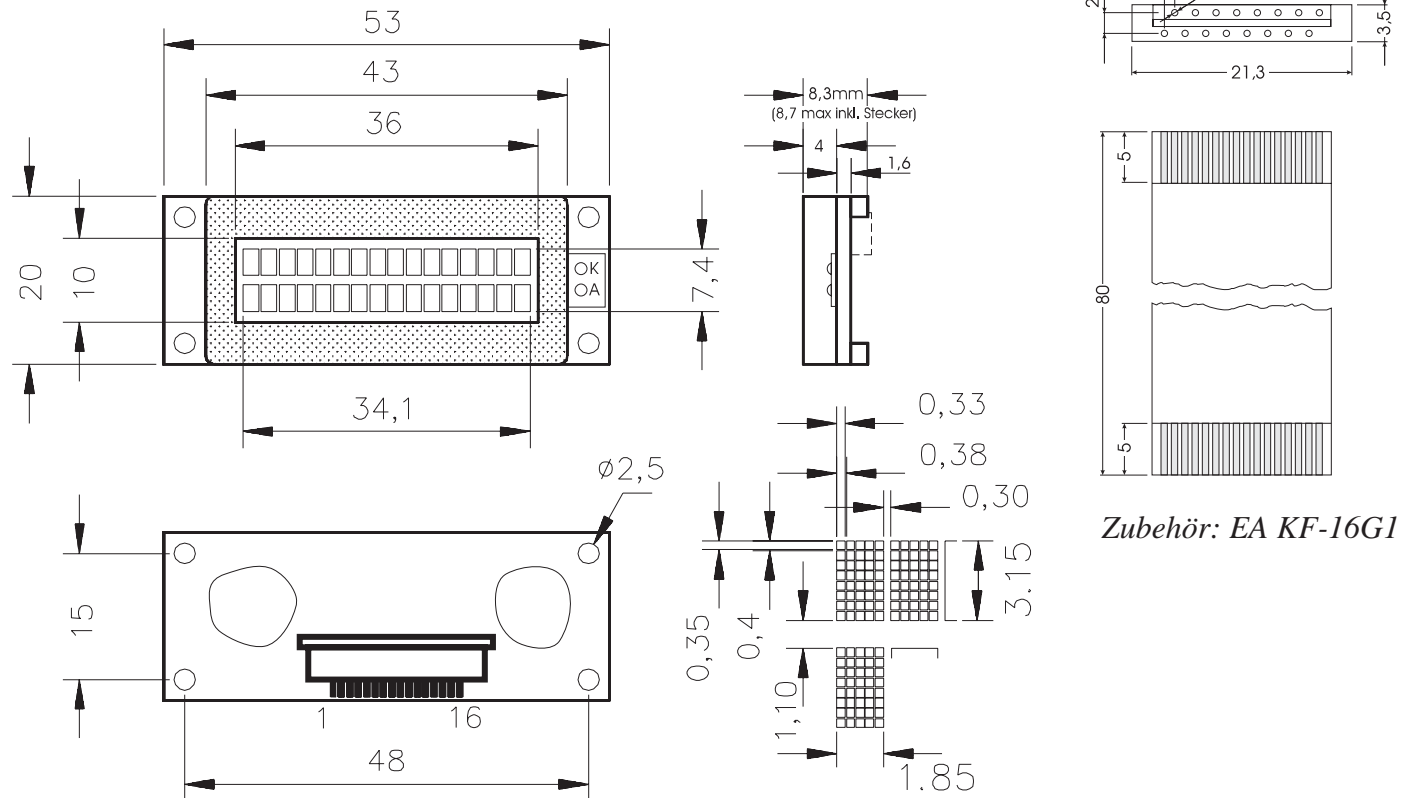
BESTELLBEZEICHNUNG

DOTMATRIXDISPLAY 2x16 / 3,15mm MIT LED-BEL. **EA T162G-3NLED**
FLEXKABEL UND NULLKRAFTSTECKER FÜR PRINTMONTAGE **EA KF-16G1**

EA T162G-3NLED

ABMESSUNGEN

(in mm)



BEFEHLSTABELLE / PINBELEGUNG

Instruction	Code										Description	Execute Time (max.)
	RS	R/W	DB 7	DB 6	DB 5	DB 4	DB 3	DB 2	DB 1	DB 0		
Clear Display	0	0	0	0	0	0	0	0	0	1	Clears all display and returns the cursor to the home position (Address 0).	1.64ms
Cursor At Home	0	0	0	0	0	0	0	0	1	*	Returns the Cursor to the home position (Address 0). Also returns the display being shifted to the original position. DD RAM contents remain unchanged.	1.64ms
Entry Mode Set	0	0	0	0	0	0	0	1	V/D	S	Sets the Cursor move direction and specifies or not to shift the display. These operation are performed during data write and read.	40µs
Display On/Off Control	0	0	0	0	0	0	1	D	C	B	Sets ON/OFF of all display (D) cursor ON/OFF (C), and blink of cursor position character (B).	40µs
Cursor / Display Shift	0	0	0	0	0	1	S/C	R/L	*	*	Moves the Cursor and shifts the display without changing DD RAM contents.	40µs
Function Set	0	0	0	0	1	DL	N	F	*	*	Sets interface data length (DL) number of display lines (L) and character font (F).	40µs
CG RAM Address Set	0	0	0	1	ACG					Sets the CG RAM address. CG RAM data is sent and received after this setting.	40µs	
DD RAM Address Set	0	0	1	ADD					Sets the DD RAM address. DD RAM data is sent and received after this setting.	40µs		
Busy Flag / Address Read	0	1	BF	AC					Reads Busy flag (BF) indicating internal operation is being performed and reads address counter contents.	-		
CG RAM / DD RAM Data write	1	0	Write Data					Writes data into DD RAM or CG RAM	40µs			
CG RAM / DD RAM Data Read	1	1	Read Data					Reads data from DD RAM or CG RAM	40µs			

Pinbelegung			
Pin	Symbol	Pegel	Beschreibung
1	VSS	L	Versorgung 0V, GND
2	VDD	H	Versorgung +5V
3	VEE	-	Displayspannung 0..0,5V
4	RS	H / L	Register Select
5	R/W	H / L	H: Read / L: Write
6	E	H	Enable
7	D0	H / L	Datenleitung 0 (LSB)
8	D1	H / L	Datenleitung 1
9	D2	H / L	Datenleitung 2
10	D3	H / L	Datenleitung 3
11	D4	H / L	Datenleitung 4
12	D5	H / L	Datenleitung 5
13	D6	H / L	Datenleitung 6
14	D7	H / L	Datenleitung 7 (MSB)
15	LED +	-	LED-Versorgung Plus /Vonwiderstand!
16	LED -	-	LED-Versorgung Minus

